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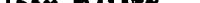
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				Application Number	09/760,063
				Filing Date	Jan. 12, 2001
				First Named Inventor	Kevin Harer
				Art Unit	2123
				Examiner Name	T. Stevens
Sheet	1	of	1	Attorney Docket Number	06816.0173

## **NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
THS		M. Kantrowitz and L. Noack. I'm Done Simulating; Now what? Verification Coverage Analysis and Correctness Checking of the DECchip 21164 Alpha microprocessor. Proceedings of 33rd DAC, 1998. pp. 325-330.	

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